



evatronix
We turn engineering into art

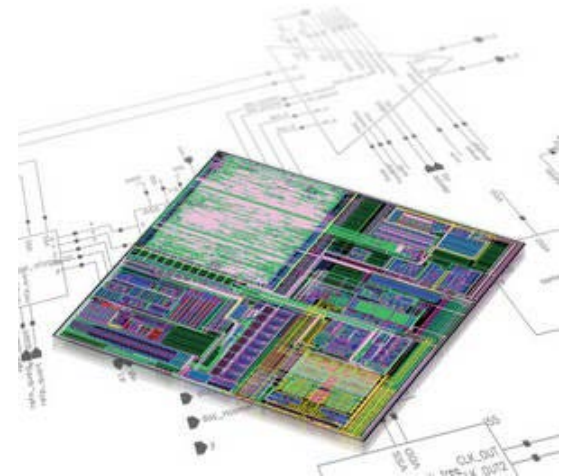
Evatronix & CMP

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Our Mission

- ◆ Develop “must-have” digital and analog IP for our customers to build cost effective solutions



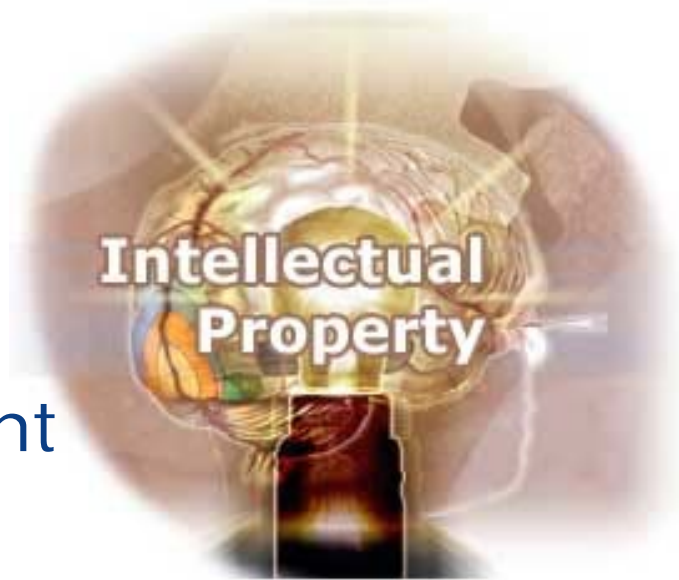
- ◆ Founded in 1991
 - Dr. Wlodek Wrona
 - ◆ Regional distribution, education
 - Dr. Wojtek Sakowski
 - ◆ IP and design services
 - Privately held
 - ◆ 90+ employees
 - ◆ 3 major facilities

- ◆ #1 Innovative Company in Poland*
 - *) Survey Computerworld PL, 2011

The European-based Global Company



- ◆ IP division:
 - 65+ engineers
 - 600+ licenses (@ ~450 customers)
 - 500+ silicon proven designs
- ◆ Business models
 - IP cores
 - Design services
 - Obsolete parts replacement



Customer Base



IP Product Portfolio

◆ Processors

- 8 - 32 bit architectures
 - ◆ 8051 family, 80251
 - ◆ 68k series
 - ◆ 80186 series
 - ◆ DSPs

◆ Interfaces

- USB 2.0, OTG & Hub
- USB 3.0
- Ethernet MACs

◆ Memory & Storage

- NAND flash
- SDIO host
- ATA/IDE host



◆ Multimedia

- JPEG 2000 encoder
- Display Controllers (HD, TV)
- I2S, SPDIF
- MIPI SLIMbus

◆ Analog-Mixed Signal

- USB2 HSIC Phy
- USB2 Full Phy
- NandFlash Phy
- PLL
- Band-Gap reference
- ..

Customer Support

◆ Customer quote:

- "... and were highly impressed with the responsiveness, quality of work and professionalism of the company...."

Director of Procurement

Fujitsu Telecommunications Europe Ltd



Business model with CMP

- ◆ Research and Education
 - 90% off list (netlist)
 - Prototyping quantities
- ◆ Commercial usage
 - 80% off list (netlist)
 - 70% off list (source code)
 - Prototyping quantities



Summary

- ◆ Complete HW/SW solutions
- ◆ Experienced design and support team
- ◆ Silicon-proven IP
- ◆ Outstanding support
- ◆ Reliable business partner
- ◆ Strong links into the Research community
- ◆ Very affordable CMP partner conditions



Thank You

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www.evatronix.com/ip