



FDSOI Training ST Microelectronics, Grenoble, France 20-21 November 2018

DATE:

28nm FDSOI training will be held at ST Grenoble site on November 20th and 21st, 2018.

AGENDA:

Wednesday November 20th: (9:30am – 5:45pm)

- Technology Overview
- Analog/RF Circuits: Benefits of FD-SOI Technology
- Technology Presentation: Process Features, Layers & Metal Stack Options
- 28FDSOI Strategy Offer
- DK Structure & Content

Thursday November 21st: (9:00am – 4:15pm)

- FE Simulation, Model & corner use
- BE Flow, Physical Verification (DRC, LVS)
- BE Flow, Parasitic Extraction (PEX), Post Layout Simulation (PLS)
- With demos.

MAXIMUM ATTENDEES:

25 people: Maxi. 2 persons by Institution. The registration is on a first come first served basis.

Deadline: 16 October 2018.

REGISTRATION:

The registration is free of charge **but limited for Institutions with NDA in place.** Registration and attendance is mandatory for both two days.

Registration will be on a first come first serve basis.

On-line Registration Form at:

<https://mycmp.fr/about-us/a-two-day-training-on-st-s-28nm-fdsoi-technology>

VENUE: ST Microelectronics, Grenoble.

Address: **12 Rue Jules Horowitz, 38000 Grenoble.**

Phone: +33 4 76 58 50 00

Access: to/from CMP/STMicroelectronics

The airport shuttle stops at "Presqu'île" (near ST by foot).

By foot (around 30 min) By tram: line B (green line, around 20 min) + walking distance in blue.

