



From layout to chips

NEW

SCAN ME!

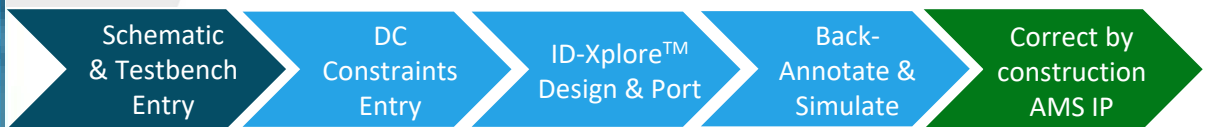
CMP makes available an EDA software for Academics & Start-Ups Pilot projects

<https://mycmp.fr/services>

SME / Large Industry, please contact us

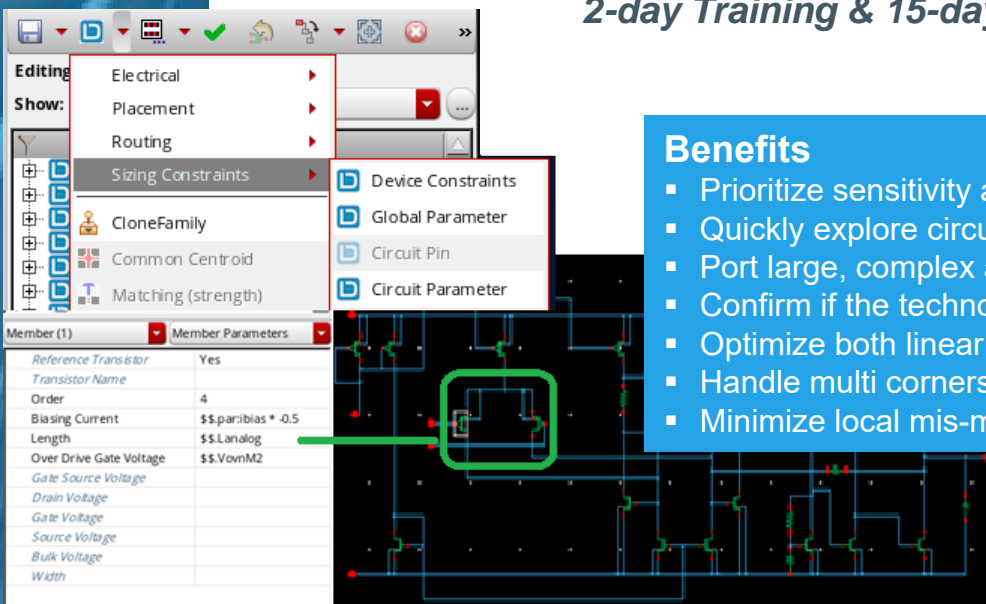


ID-Xplore™



- quick design trade off evaluation
- rapid derivative design generation
- fast technology porting

2-day Training & 15-day Licence package available



Benefits

- Prioritize sensitivity and design trade-offs
- Quickly explore circuit changes to meet specifications
- Port large, complex analog IP blocks in hours
- Confirm if the technology PDK suits design topology
- Optimize both linear and non-linear circuits
- Handle multi corners analysis
- Minimize local mis-match effects

Constraint edition fully integrated into Cadence environment

Contact us

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