



From layout to chips

Add-On Services



European
Commission

Horizon 2020
European Union funding
for Research & Innovation



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The Add-On Services purpose

Academics / Industrials

Showcase

CMP Add-On Services webpage

Useful IP Blocks

Design expertise

Test Bench &
Characterization facility

Software and design
Routines

IC Inspection &
Repairs

System Level and
Packaging Services

Find solutions

CMP Website visitors

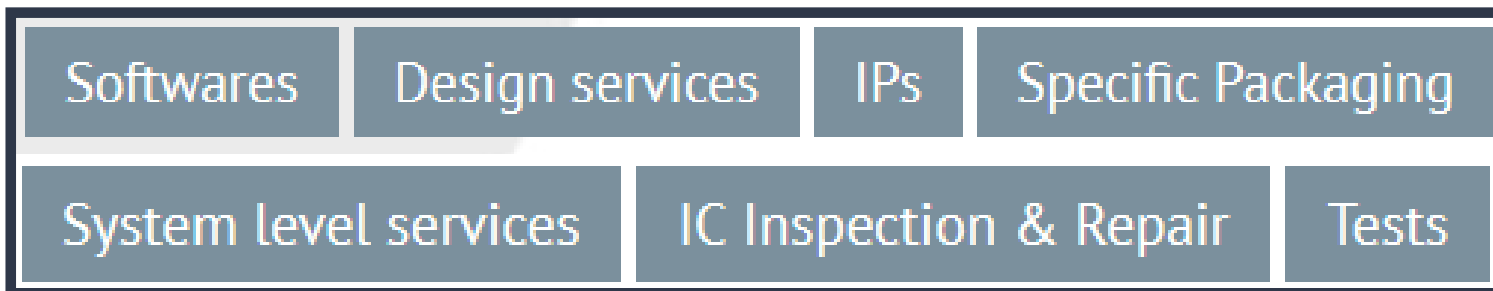


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Objectives & categories of services

- **Provide visibility** to specific and useful services
 - Visibility for the providers
 - Unique service catalog for the users
- **Consolidate CMP** as a core provider of solutions for prototyping services

Categories of companion services:





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Add-On Services browser

Technologies Services Requests About us Press Contact Login mycmp

CMP Home > Services > Add-On Services Print to file

Add-On Services

You're interested in highlighting a specific industrial service, a design expertise, an important thesis, an innovative IP, a characterization facility, a process or a methodology you improved or a specific/original tool you created?

|| CMP is currently looking for industrials and academics to grow the Add-On Services showcase

This webpage is a showcase meant to highlight specific services from companies or academics in order to provide advanced and qualitative prototypes service to our R&D community.

You'll find services completing our prototyping offers: from advanced design services to innovative packaging solutions, discover here original services that may improve your prototyping experience!

CMP takes no fee in these services and does not act as an intermediary between the provider and the user. The goal of this showcase is solely to develop the R&D community in microelectronics giving visibility to advanced and original processes reviewed by CMP.

Contact us

✉ Jérémy PERRET

ALL

Softwares Design services IPs Specific Packaging System level services IC Inspection & Repair Tests

Intento Design Softwares RESPONSIVE EDA FOR ANALOG IP	VLC Photonics Design services PHOTONICS	ALGODONE IPs algodone	HCM SYSTREL Specific Packaging SERMA HCM.SYSTREL	System level services 1 System level services
IC Inspection & Repair 1 IC Inspection & Repair	Liberty Test Tests Liberty test	NoiseTech Microwaves Tests NoiseTech Microwaves Ltd.	ID MOS Design services SERMA ID MOS	ReRouting LLC. Design services ReRouting
Ironwood Electronics Tests Ironwood ELECTRONICS	APTASIC SA Tests aptasic supplying customized ICs	Synergie Cad PSC Specific Packaging SYNERGIE CAD PSC PRIMA SEMICONDUCTORS	RFIC-Lab Design services RFIC-Lab Grenoble	

Dedicated webpage

Categories of Services

Companies / Labs already in AOS

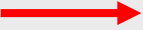


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Illustration

The screenshot shows the NoiseTech Microwaves website. At the top is a navigation bar with links for Technologies, Services, Requests, About us, Press, and Contact. Below the navigation bar is a breadcrumb trail: CMP Home > Services > Add-On Services > NoiseTech Microwaves. The main content area features the company name 'NoiseTech Microwaves' with a logo, followed by 'Tests' and a detailed description of their services. The description includes equipment highlights such as wideband operation, ultra-repeatability, and compact size. At the bottom of the page, there are two photographs of NoiseTech microwave noise source units.

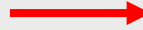
Company name



NoiseTech Microwaves

Easy access on CMP website

Main services



Tests

NoiseTech develops and markets impedance generators, noise sources, switches for measuring noise parameters. NoiseTech's serves designers for wireless/wired components, GNSS receivers, semiconductor industry, R&D laboratories, automatic test equipment, etc. NoiseTech offers noise-parameter measurement services for connectorized devices and circuit operating in the 100MHz-to-6GHz frequency range. NoiseTech measurement equipment consists of NoiseTech's impedance generator, noise source, and switch.

Equipment highlights: *Impedance generators:*

- Wideband operation (100MHz to 6GHz, contact NoiseTech for lower frequency range)
- Ultra repeatable (repeatability of >80dB)
- Ultra fast for fast measurements
- Compact
- USB driven
- Internal temperature sensor for better accuracy

Noise source:

- Controlled directly controlled with the impedance generator
- Flat output power spectrum
- Various ENRs available
- Temperature sensor improves accuracy

Switch:

- USB controlled
- Seamlessly integrates with the impedance generator
- Improves measurement uncertainty

NoiseTech can assist with identifying and configuring a noise-parameter measurement system that matches customers measurement challenges.

Service description



The CONTACT section of the website includes a 'Print to file' button, the NoiseTech Microwaves Ltd. logo, contact information (support@noisetechmicrowaves.com), the company URL (http://noisetechmicrowaves.com), and a link to DOCUMENTATION.

Company or lab logo



Direct contact link



Direct link to company or lab website



Illustrations of services





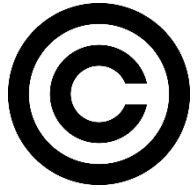
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Overview of specific services

Companies already in AOS

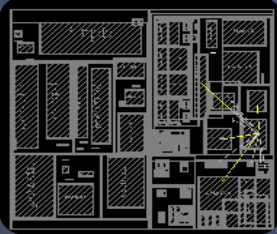
<p>Intento Design</p> <p>Analog Design Softwares Optimization</p> <p>RESPONSIVE EDA FOR ANALOG IP</p>	<p>VLC Photonics</p> <p>Photonics Design services Design</p>	<p>ALGODONE</p> <p>IPs IP Licensing & Protection</p>	<p>HCM SYSTREL</p> <p>Power Modules & Specific Packaging Optical Devices</p>
<p>ReRouting LLC.</p> <p>Design services Emerging Non-Volatile Memories</p>	<p>Liberty Test</p> <p>Tests ESD & Latch-Up Test</p>	<p>NoiseTech Microwaves</p> <p>Tests Noise Parameters Measurement</p>	<p>ID MOS</p> <p>Design services Extreme Temperatures</p>
<p>Ironwood Electronics</p> <p>Socket Solutions Tests</p>	<p>APTASIC SA</p> <p>Tests ASIC Test Center</p> <p>supplying customized ICs</p>	<p>Synergie Cad PSC</p> <p>Specific Packaging</p> <p>Moulded Plastic Packages</p>	<p>RFIC-Lab</p> <p>Design services RF & millimeter</p>

Academics can contribute with



Intellectual Property

- Academics who work on specific IPs can promote them.



Design expertise

- Academics can have a design expertise and help designing chips.



Testing

- Academics might have advanced machines to test chips.

Do not hesitate to make suggestions, we are open to new ideas !



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AOS Community IP sharing

- IP blocks are desirable **to ease and speed up design**
 - **Affordable access to IP** for R&D purposes
 - **Non qualified but tested IP blocks** could be shared through CMP !
 - CMP manages the collection of IPs that users are ready to share
 - Terms and conditions:
 - Free of charge**, rewards with references or coauthoring or others
- Note: IP sharing is only possible through CMP** in agreement with confidentiality licenses (CLA) !!!



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AOS Terms and Conditions

- Service **free of charge** for providers and users !
- Only **original** and **specific** services to serve R&D and prototyping purposes
- Only a **publicity agreement** to be signed
- Users and providers are **directly interacting**
- Services proposal to be submitted within a **specific frame**