



From layout to chips

# myCMP: MPW specialized web application

CMP annual users' meeting - 07-Feb.-19 - PARIS



Horizon 2020  
European Union funding  
for Research & Innovation



From layout to chips

# Agenda

- Motivation
- Overview of myCMP web application
- Main features / benefits
- User interface

Registration page

Central control panel

Design kit requests

Run status

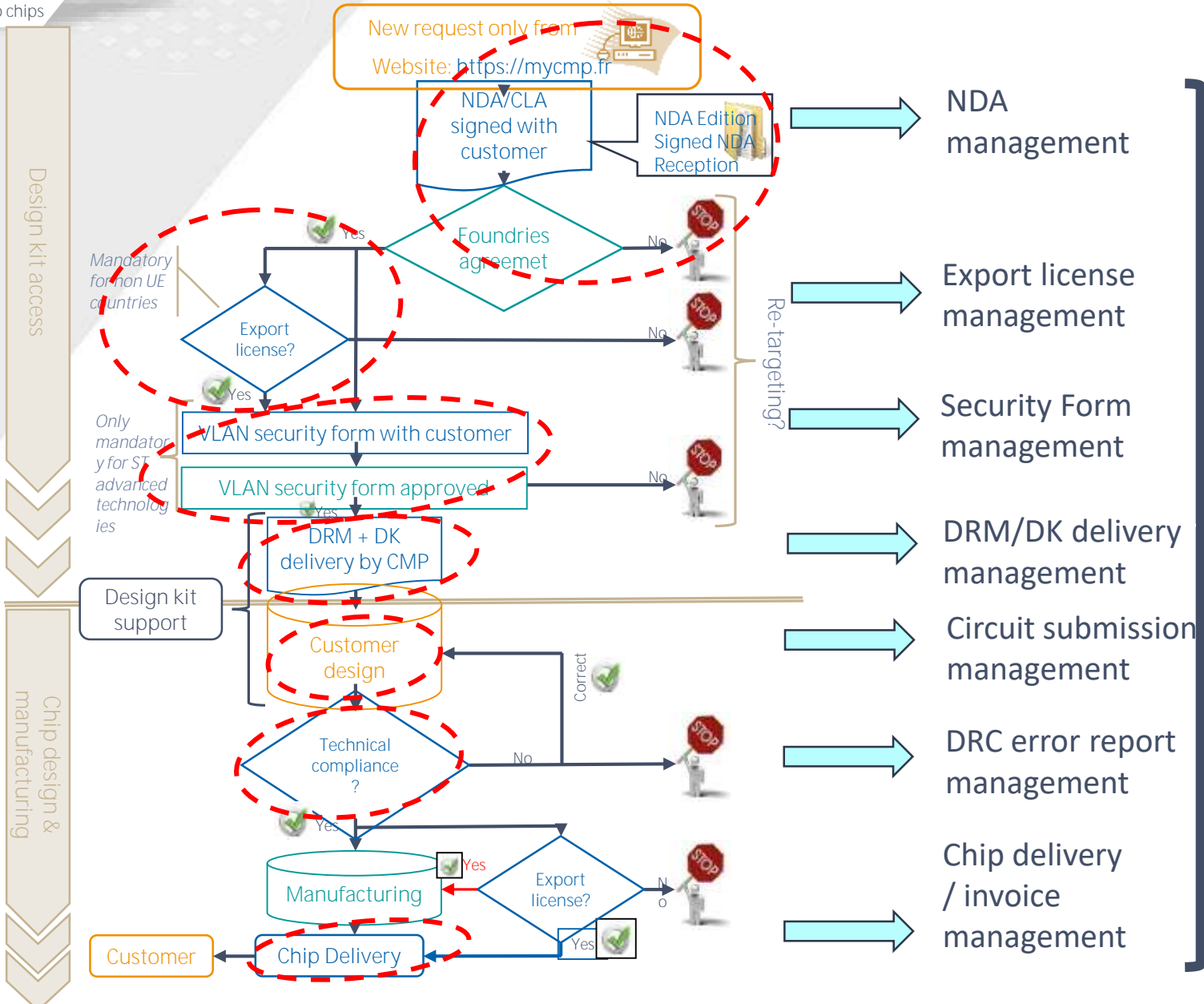
DRC report interface

- Mile stones
- Conclusion



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# Motivation



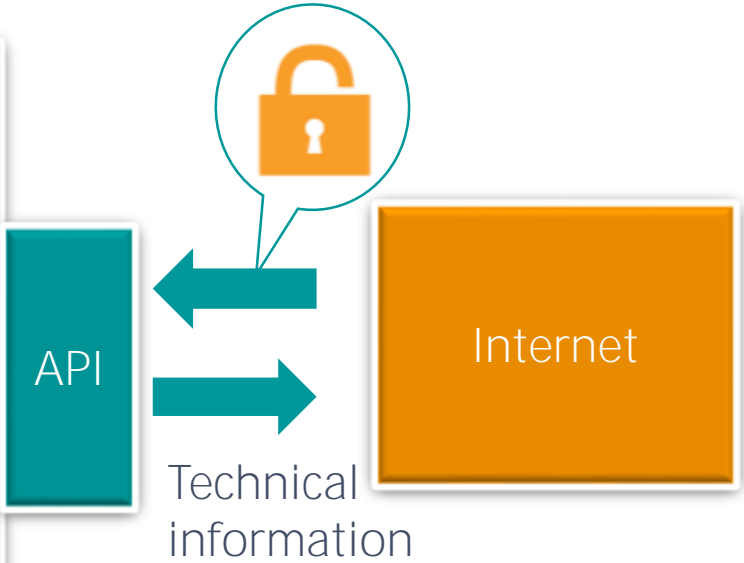
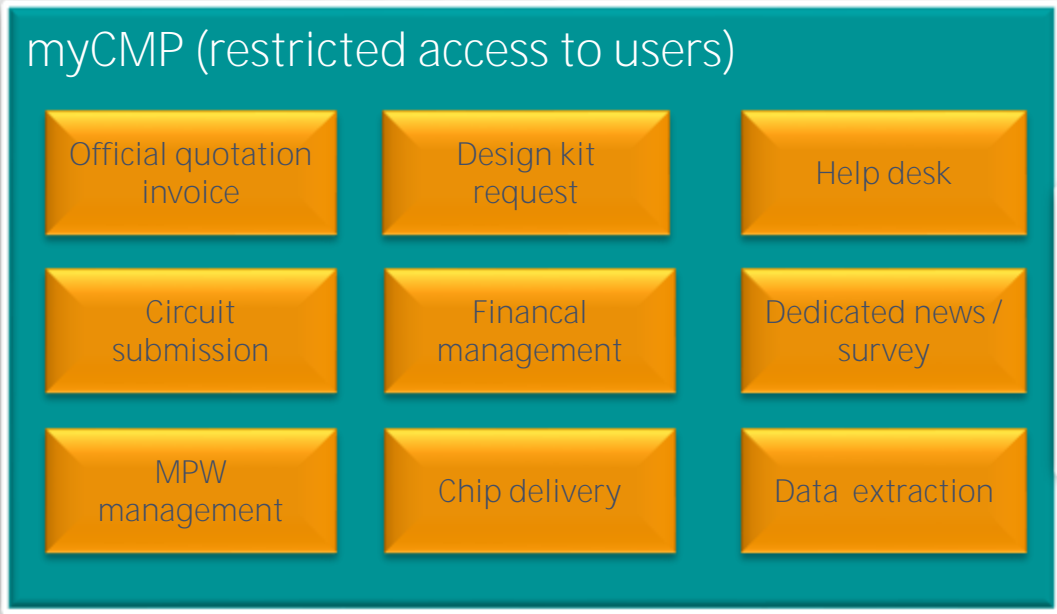
**All in one tool/ interface: myCMP**



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# myCMP overview

myCMP (restricted access to users)



On-line secure, ergonomic interface between designers, CMP staff and manufacturers unifying all provided services





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# Main Features / benefits

- All CMP services in one unified tool/interface
- Interconnection of different CMP core activities
- Automated tracking / Real time status of the project
- Single exchange platform for all documents
- Direct chat with CMP run managers
- Embedded design kit support center
- Data and document archiving



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# Registration Form

MyCMP : Sign up

https://cm.mycmp.fr/register

**Sign up**

[Go back](#)

Welcome to MyCMP, please create an account to access our services by filling in the fields below.

### Your institution

**Institution \***

Name of institution

**Activity type \***

Education  Research  Industry

Are you Europractice-IC Member?

**Fill in your Europractice Member ID \***

Europractice Member ID (ex : R00001)

**Head office**

**Address \***

Address line 1

Address line 2

**Zip Code \*** **City \***

Zip Code City

**State / Province / Region** **Country \***

State / Province / Region France

**Technical Manager**

**Legal liability**



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# User Central Control Panel

The screenshot shows a user interface for a design kit management system. The top navigation bar includes a logo, the text "From layout to chips", and a user profile for "Alejandro MANAGER". A notification bell icon is circled in red with the text "Notifications from CMP staff".

The main content area is titled "Dashboard" and contains several widgets:

- Design kit requests in progress:** A widget showing a request for "Request n°89" submitted by "Mr MANAGER of Inst\_AC institution". It includes details like "BICMOSS5" and "Pending unlocking by CMP". This widget is circled in red with the text "Summary of DK requests".
- My current circuits:** A widget showing a circuit named "top\_cell" with the status "FTP Submission pending". This widget is circled in red with the text "User project status at a glance".
- Requests that expire:** A widget listing requests that are about to expire, such as "Your circuit 'top\_cell' is now under study" (6 minutes ago).
- My current reservations:** A widget listing reservations, such as "Reservation n°56" and "Reservation n°55".

A left sidebar contains navigation links: "Dashboard", "Design kit requests", "Run", "Users", and "Settings". This sidebar is circled in red with the text "Main modules".

On the right side, there is a "Notifications" panel with a "Mark all as read" link and a "Choose widgets to display" dropdown. The notifications list includes:
 

- Your request for the design kit in BICMOSS5 technologies is under review. now
- Warning ! The file ams\_top\_cell.gds sent via FTP for your circuit 'ams\_top\_cell' is not in the required format. Please compress it in GDS.GZ format. 3 minutes ago
- Your circuit 'top\_cell' is now under study. 6 minutes ago
- A new FPT access has been opened for your order (n°60) of circuit: ams\_top\_cell. Your login is 98794e2. You can now configure the password for this FTP access. 24 minutes ago
- A new FPT access has been opened for your order (n°61) of circuit: top\_cell. Your login is 2626e52d. You can now configure the password for this FTP access. 24 minutes ago





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# myCMP Design kit request

The screenshot shows the 'Design kit request n°72' interface. A sidebar on the left contains navigation options: Dashboard, Design kit requests, Dashboard, Records, New request, View, Users, and Settings. The main content area is titled 'Design kit request n°72' and includes a 'Request summary' section with fields for status, date, expiration, legal responsible, requested technologies, delivery address, project of a structure, and users with access. Below this is a 'DK request chat area' with a chat window and a 'Send' button. At the bottom, there is a 'Legal / security documents management interface' showing four document cards: NDA, DKLA, FS VLAN Security form, and FFS VLAN security form, each with a status and a 'Change the signed document' button.

Request summary

DK request chat area

Legal / security documents management interface





# Run Status

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**Order processing steps**

**Order summary**

**Order technical info**

**Design submission chat area**

**Reservation/order user interface**

myCMP

John DOE

Dashboard

Design kits requests

Run

Dashboard

Make a reservation

My reservations

Place order

My orders

Users

Back to list

Circuit Order Address **DRC**

Order Data processing ST : Design Review ST : Floorplan validated ST : Lot started Post manufacturing Decoding Delivered

Status : Validated

Circuit name : RTX2080

Reservation : 08/21/2018 (6\*4)

Run : 528116\_TEST (09/06/2018)

Delivery date : 10/16/2018

Technology : CMOS28FD-50I

Dies thickness : 370 μm

Size of circuit : 2500 x 2500 μm (6.25 mm²)

Circuit function : plop

Other information :

Technical information

User in charge : Mr John DOE

Technical manager : Mr John DOE

Legal responsible : Mr John DOE

Design kit used : CMOS28FD50I - PDK 2.9 (2.9)

Kit libraries used : virtuoso (1.7)

Libraries du kit : C2850I\_SC\_8\_SHIFT\_LL (5.2-00)

Utilisateurs :

Design order ?

Envoyer



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# DRC report interface

Order identification

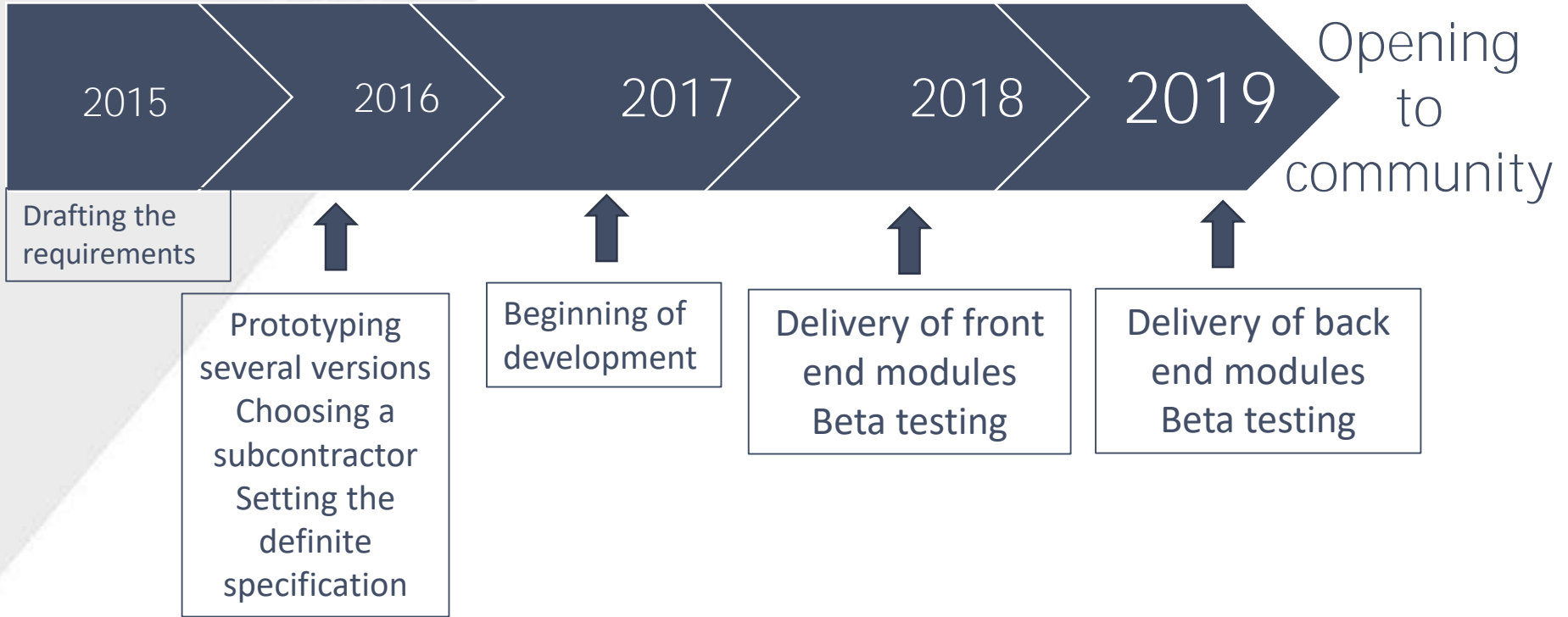
File transfer interface

Access to report



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# myCMP milestones





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# Conclusion

- We hope myCMP will be reliable

efficient

simplified

securized

unified

- Access

<https://mycmp.fr>