Evatronix & CMP

An Introduction

Michal Jedrak
Technical Marketing Manager
Develop “must-have” digital and analog IP for our customers to build cost effective SoC solutions
20 YEARS IN BUSINESS

- **Founded 1991**
  - Dr. Wlodek Wrona
    - Regional distribution, education
  - Dr. Wojtek Sakowski
    - IP and design services
- **Privately held**
  - 90+ employees
  - 3 R&D facilities
  - 3 direct sales offices
THE EUROPEAN BASED GLOBAL COMPANY

**IP division:**
- 60+ engineers
- 600+ licenses (@ ~450 customers)
- 450+ silicon proven designs

**3-pod business model**
- IP cores „off the shelf“
- Design services
- Obsolete parts replacement
DIGITAL IP PRODUCT PORTFOLIO

ﻼProcessors
- 8 - 32 bit architectures
  - x51 series, Z80, 6502
  - 68k series
  - 80186 series
  - DSPs

ﻼInterfaces
- USB 2.0, OTG & Hub
- USB 3.0
- Ethernet MACs

ﻼMemory & Storage
- NAND flash
- SDIO host
- ATA/IDE host

ﻼMultimedia
- JPEG 2000 encoder
- Video Controllers (HD, TV)
- I2S, SPDIF

ﻼCommunication
- HDLC
- SDLC
- I2C, SPI
**Customer quote:**

“.... and were highly impressed with the responsiveness, quality of work and professionalism of the company....”

*Director of Procurement, Fujitsu Telecommunications Europe Ltd*
Partnerships & Associations

- ARM
- ACAP
- SDI0
- Xilinx Alliance Program
- Universal Serial Bus
- The SPIRIT Consortium
- MIPI
- OCP
- Evatronix
Research and Education
- 90% off list (netlist)
- Prototyping quantities

Commercial usage
- 80% off list (netlist)
- 70% off list (source code)
- Prototyping quantities
SUMMARY

- Complete HW/SW solutions
- Experienced design and support team
- Silicon-proven IP
- Outstanding support
- Reliable business partner
- Strong links into the Research community

- Very affordable CMP partner conditions
Thank You

www.evatronix.com