








From layout to chips

# Annual meeting - agenda

<b>08:45</b>	<b>Welcome coffee</b>		
08:45 - 09:15	Introduction	J-Christophe Crébier	CMP
09:15 - 09:30	2019 Activity Results	Kholdoun Torki	CMP
09:30 - 09:50	Printed Electronics	Benoit Piro	<b>Univ. Paris Diderot</b>
09:50 - 10:05	0.35µm, 0.18µm Technologies	Lyubomir Kerachev	CMP
10:05 - 10:20	STMicroelectronics MPW services	Romain Verly	CMP
10:20 - 10:30	NANO2022 program	J-C Houdbert, Andreia Cathelin, François Martin	<b>STMicroelectronics</b>
<b>10:30 - 11:00</b>	<b>Break discussion</b>		
11:00 - 11:20	SMIC technology offer through CMP	Elisabetta Chierici, Massimo Bonanomi	<b>SMIC</b>
11:20 - 12:00	<b>Panel on Photonics ICs:</b>	Moderator: Romano Hoofman	<b>IMEC</b>
	VTT Si-photonic tech. on 3µm SOI platform	Matteo Cherchi	<b>VTT</b>
	CEA-LETI Si310-PHMP2M technology	Christophe Kopp	<b>LETI</b>
	AMF foundry offer through CMC-CMP partnership	Peter Stokes	<b>CMC</b>
	Teem Photonic's glass photonic platform, coupling solutions fiber-die	Arnaud Rigny	<b>Teem Photonics</b>
	Photonics MPW services @ CMP	François Berthollet	CMP
12:00 - 12:20	More than Moore technologies	Lyubomir Kerachev	CMP
12:20 - 13:00	<b>Panel on new developments @ CMP:</b>	Moderator: Kholdoun Torki	CMP
	Teaching module IP: from design to test	Sylvain Bourdel	<b>RFIC Lab</b>
	myCMP: one year of use	Christelle Rabache	CMP
	Add-On Services /IP	Jérémy Perret	CMP
	Visual inspection services	Nicolas Partenza	CMP
13:00 - 13:10	Closing remarks	Kholdoun Torki	CMP
<b>13:10 - 14:30</b>	<b>Lunch discussion</b>		

# Access conditions of the photonics technologies available through CMP

		-	CEA-LETI	}	Already available
		-	AMF		
		-	Teem Photonics	}	Available soon
		-	VTT		



## Offering terms :

- 2 runs scheduled per year : one in March and the other one in October
- Rate per area : 1,600€/mm<sup>2</sup> for the first 10mm<sup>2</sup>  
1,000€/mm<sup>2</sup> for the following ones
- A circuit whose area is 20mm<sup>2</sup> will be charged  $1,600 \times 10 + 1,000 \times 10 = 26,000$ €
- Minimum quantity of dies supplied : 25
- Leadtime : between 10 and 12 months
- Supported PDK softwares : Cadence IC, Synopsys OptoDesigner and Mentor-Tanner L-Edit for the design, and Mentor Eldo and Calibre for simulation and verification

## 2020 expectations :

- Development of a SiN brick for 900nm wavelength applications
- Switch from a 200 to a 300mm wafer platform

## Offering terms :

- 2 runs scheduled per year : one in March and the other one in November
- Rate per block : 7,500€ for 6mm<sup>2</sup>  
12,000€ for 12mm<sup>2</sup>  
20,100€ for 24mm<sup>2</sup>  
37,900€ for 48mm<sup>2</sup>
- Minimum quantity of dies supplied : 20
- Leadtime : between 5 and 6 months
- Supported PDK softwares : Mentor Pyxis and Luceda IPKISS-Mentor/Tanner L-Edit for the design, and Mentor's tools for the simulation and verification

} For academia only

} For both academia and industry

## 2020 expectations :

- Addition to the PDK of a new compact model library from Lumerical
- SiN waveguide capability integrated into the Si-photonics process
- 2 trainings offered per year, one in passive Si-photonics, and another in active



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# Teem Photonics technology



## Offering terms :

- 4 runs scheduled per year : one every 3 months
- Rate per wafer or half-wafer
- Maximum of 30 dies per wafer
- Leadtime : 1 month
- Supported PDK software : NAZCA (free and open-source)
- WAFT and packaging prices will be disclosed soon

} Custom PIC

## 2020 expectations :

- Design kit release planned for the end of the first semester
- Switch from a 60 to a 100mm wafer platform



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# VTT Si-photonics technology



Negotiations underway  
More information available soon